

DOCKET NO.: SHIX-CN020002 (STNX01-20002)

PATENT

Customer No.: 84274

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re application of : Jigang Liu

Application No. : 10/500,620

Filed : July 1, 2004

For : TRANSCEIVER WITH MULTI-STATE DIRECT DIGITAL
SYNTHESIZER DRIVEN PHASE LOCKED LOOP

Art Unit : 2618

Examiner : Nguyen, Tuan Hoang

Confirmation No. : 4330

MAIL STOP RCE

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

INFORMATION DISCLOSURE STATEMENT

Pursuant to the duty of disclosure under 37 C.F.R. § 1.56, Applicant submits this statement.

This submittal is made in accordance with 37 C.F.R. §§ 1.97 and 1.98 and § 609 of the Manual of Patent Examining Procedure. The references herein are listed below and on the attached Forms PTO/SB/08A & B. Copies of the listed references are submitted herewith.

<u>Foreign Patent No.</u>	<u>Country</u>	<u>Date</u>
JP 1989-208005	Japan	Aug. 22, 1989
WO 92/22147 A1	PCT	Dec. 10, 1992

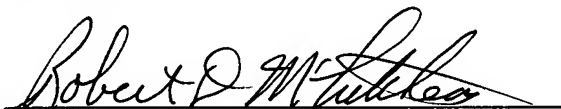
Publication

International Search Report dated May 8, 2003 in connection with PCT Application No. PCT/IB02/05349.

Applicant hereby expressly reserves the right to swear behind the effective dates of any of the above Patents and to question the relevance and materiality of the Patents and Publications listed herein, in whole, in part, or in combination, subsequent to filing this Information Disclosure Statement.

Respectfully submitted,

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Date: 2/9/2009

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